(12) **PATENT** (11) Application No. AU 199864844 B2 (19) AUSTRALIAN PATENT OFFICE (10) Patent No. 748263 (54)Operation of gaming machines in a linked bonus prize winning mode International Patent Classification(s) A63F 005/04  $(51)^7$ (21) Application No: 199864844 (22)Application Date: 1998.05.11 (30)**Priority Data** (33) Country (31)Number (32) Date PO6741 1997.05.09 ΑU (43)Publication Date: 1998.11.12 (43)Publication Journal Date: 1998.11.12 Accepted Journal Date: 2002.05.30 (44) (71)Applicant(s) I.G.T. (Australia) Pty. Limited; Acres Gaming, Inc. (72)Inventor(s) **John Francis Acres** (74)Agent/Attorney SPRUSON and FERGUSON, GPO Box 3898, SYDNEY NSW 2001 (56)**Related Art** AU 70247/96 US 6110043 AU 20986/92

#### **ABSTRACT**

# OPERATION OF GAMING MACHINES IN A LINKED BONUS PRIZE WINNING MODE

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A gaming machine system (10) having a bonus prize award mode in the play of gaming machines (1-5) is disclosed. Interface units (36) each are coupled to a respective gaming machine to receive a play signal that includes the identity of the respective machine. A central processor (50), that includes a data memory (55) is linked to each of the interface units (36). The system is operable by the processor (50) executing a first prize accumulation phase during which a value is added to an initial value for each play signal up to a level where the accumulating value equals a prize value that will be awarded to one of the gaming machines, and executing a second prize award winning phase during which the accumulated value remains frozen and subsequent plays of the gaming machines are separately counted for their occurrence. The play of a gaming machine causing the count to equal a prize win count value results in that gaming machine being awarded the prize value.

#### **AUSTRALIA**

#### **PATENTS ACT 1990**

#### **COMPLETE SPECIFICATION**

#### FOR A STANDARD PATENT

#### **ORIGINAL**

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Invention Title:

Operation of Gaming Machines in a Linked Bonus Prize

Winning Mode

ASSOCIATED PROVISIONAL APPLICATION DETAILS

[31] Application No(s)

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9 May 1997

The following statement is a full description of this invention, including the best method of performing it known to me/us:-

## OPERATION OF GAMING MACHINES IN A LINKED BONUS PRIZE WINNING MODE

#### Field of the Invention

This invention relates to the field of gaming machines, and particularly to a number of such machines that are linked in a network sense to provide a bonus prize winning mode of operation. The bonus prize winning mode is additional to wins occurring in play of the normal game(s) provided by each gaming machine. In one particular form the gaming machines are spinning reel or card-type machines commonly known as slot or fruit machines.

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#### **Description of the Prior Art**

It is known to provide systems that implement bonus prize winning modes apart from the normal games available for play on a gaming machine. Such systems typically comprise a bank of gaming machines each having an interface providing communication with a central controlling computer or data processor. The controlling computer receives from each connected gaming machine an indication of each play (and possibly also the wager value). A bonus prize pool is formed and accumulates with each play of the gaming machines. This accumulating total most usually is prominently displayed on a visual display unit. The bonus pool is awarded when it reaches a (usually) randomly chosen value, being won by the player of the machine whose play caused the total to be reached. Examples of such prior art arrangements can be noted in Australian Patents Nos. 589158 and 655801.

Another prior art arrangement is described in published Australian Patent Application No. 70247/96.

A key motivation or desire of operators of licensed premises in which gaming machines are placed is to encourage the machines to be in use as much as possible since usage directly affects the revenue gained. As such a bonus prize winning scheme encourages and promotes usage by the prospect of a windfall prize independent of

won in normal play of the gaming machines. It is accordingly desired to develop improved methods of operating a prize winning mode to encourage greater machine usage, also to provide enhanced enjoyment for the players. The present invention seeks to achieve these objectives.

#### **Summary of the Invention**

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Therefore, the invention, in one broad form, discloses a method for awarding a bonus prize in the play of a plurality of gaming machines, the gaming machines being linked to a central processor having a data memory, the method comprising the steps of:

transmitting, on play of any one gaming machine, a play signal therefrom to said central processor, said play signal identifying the transmitting gaming machine;

registering each said play signal as an entry in said memory

executing a prize accumulation phase during which a value is accumulated with an initial value for each play signal entry up to a level where the accumulating value equals a prize value that will be awarded to one of the linked gaming machines; and thereafter executing a prize awarding phase during which said accumulated value remains frozen, and subsequent plays of one or more of the gaming machines are separately counted for their occurrence, and a play of a gaming machine causing the count to equal a prize win count value results in that gaming machine be awarded said prize value.

In one advantageous form there comprises the further step of displaying the awarding of said prize value on an indicator means and/or the respective gaming machine.

The invention further discloses a gaming machine system having a bonus prize awarding mode in the play of gaming machines, the system comprising:

a plurality of gaming machines for playing games thereon;

a plurality of interface units each coupled with a respective gaming machine to receive a play signal therefrom including the identity of the respective gaming machine;

a central processor having a data memory and being linked to said interface units to receive said play signals; and



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indicator means lined with said central processor;

and wherein said system is operable by said central processor executing a first prize accumulation phase during which a value is added to an initial value for each said play signal entry up to a level where the accumulating value equals a prize value that will be awarded to one of the linked gaming machines, causing said accumulating value to be displayed on said indicator means, and executing a second prize awarding phase during which said accumulated value remains frozen, and subsequent plays of the gaming machines leading to subsequent memory entries are separately counted for their occurrence, and a play of a gaming machine causing the count to equal a prize win count value results in that gaming machine be awarded said prize value.

Preferably, said indicator means and/or the respective gaming machine displays the awarding of said prize value.

The invention further provides a method of operating gaming devices interconnected by a network to a central processor, comprising the steps of:

determining a prize amount;

transmitting an identifying signal from each gaming device to the central processor responsive to play of the gaming devices;

using the processor to track the amount of money played on the gaming devices; allocating a portion of the money played ont eh gaming devices to a bonus pool during a prize accumulation phase;

thereafter continuing to transmit an identifying signal from each gaming device to the central processor responsive to play of the gaming devices during a prize awarding phase;

comparing gaming device play during the prize awarding phase to a predefined level of gaming gaming device play; and

awarding the prize amount to a selected gaming device after the gaming device play during the prize awarding phase equals the predefined level of gaming device play.

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Both the method and system of the invention can provide that the play signals generated in the prize accumulation and/or prize awarding phases are based on any one or more of coin-in, coin-out, play duration and quantum of wagers. The prize awarding phase further can be based on a chosen fixed value of play signals.

#### **Brief Description of the Drawings**

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An embodiment of the invention will now be described with reference to the accompanying drawings, in which:

Fig. 1 shows a general arrangement of components of a bonus prize award winning system;

Fig. 2 shows a schematic block diagram of a bonus prize award winning system embodying the invention;



Fig. 3 is a flow diagram of the bonus prize winning cycle;

Fig. 4 is a flow diagram of the prize accumulation prize; and

Fig. 5 is a flow diagram of the prize awarding phase.

#### 5 Description of Preferred Embodiments

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As shown in Fig. 1, a bonus prize award winning system 10 comprises five gaming machines 1-5 in a bank or row configuration. These gaming machines allow the players thereof to participate in the bonus prize winning feature. The gaming machines 1-5 can be situated in a casino or other licensed premises. There may be other such gaming machines that do not allow players to participate in the bonus prize winning feature. Alternatively, in a site of some hundreds of gaming machines, all could participate in the feature.

Each of the gaming machines provides for the play of a game such as a spinning reel or simulated poker card game. Each gaming machine 1-5 is played independently of any other, although the individual plays collectively contribute to the bonus prize winning feature.

A display unit 20 is located above the gaming machines 1-5 to provide visual and audible indications to players of the progress and completion of the bonus prize winning feature. The display unit includes a prize value display 22, a winning machine display 24 and a horn loudspeaker 26, the functions of which will be described presently.

Each gaming machine has an internal processor 30 (not shown), a reel display unit 32 by which a player observes the game play and a series of inputs 34 such as game operation pushbuttons and a coin acceptor (or card equivalent) for wagering on plays of the game.

Referring now to Fig. 2, there is shown a schematic block diagram of the system 10. Each of the gaming machines 1-5 further comprises an interface unit 36 providing communication between the processor 30 by a respective data link

40,42,44,46,48. These links communicate with a central controlling unit 50, having an interface unit 52, a processor 54 and an associated memory 55. The processor 54 in turn communicates with the display unit 20 by a further data link 56.

The gaming machines can be the Game King<sup>™</sup> type of machine manufactured by the present co-applicant I.G.T. (Australia) Pty Limited. The data links, central control unit 50 and the display unit 20 can be of any of the types manufactured by and available from the other co-applicant, Acres Gaming Inc. The bonus prize winning methodology is, in large measure, achieved by programming of the central processor 54.

The bonus prize winning operation is performed in discrete cycles, each of which contains two sequential phases, as shown generally in the flow diagram of Fig. 3. Upon initialisation, the prize accumulation phase takes place followed by the prize awarding phase, at which time the bonus prize is awarded. If the operation is to continue the cycle repeats. The "continue" test is under the control of a system operator having control over the central controlling unit 50.

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Fig. 4 shows the prize accumulation phase in greater detail. Firstly, a "bonus pool" is reset to an initial value typically chosen in a range. For the purposes of explanation, let the range be in the range \$100-\$500 representing the possible prizes that can be won. The prize need not only be in a monetary denomination, rather can be in any form of units or points have money or money's-worth. The initial value is chosen to be \$100.00.

Next, any "excess" is added to the initial value. For the first cycle the excess value will be \$0.00. This aspect will be returned to later.

Next, the prize value is selected as a random value in the range of values (initial value + excess) to the maximum value, ie. \$100-\$500, and for example is \$450.25.

Next, each play of any of the gaming machines 1-5, resulting in a play signal on lines 40-48, is counted in a cumulative manner as an event. This can be either as

plays of the game (coin-in) or units wagered (coin-out). In the present embodiment, each count accumulates as a percentage of the monetary units wagered, ie. 2%. The prize pool thus grows with play of each gaming machine, and with each input the identity (ie. machine number) of the respective gaming machine is also noted and stored in the memory (possibly only temporarily).

The accumulating total is displayed on the prize value display.

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Next, the central processor checks on each input whether the count equals the prize value of \$450.25. When this condition is satisfied, the bonus prize is 'frozen' as is the state of the prize value display 22. Also, a visual and/or audible indication of the bonus prize value having been reached is given by flashing lights or bells, for example. It is important to realise that the bonus prize value is not awarded as a matter of course to the player of the gaming machine whose play has taken the accumulated pool to the prize value, as is the case for the prior art Australian patents noted above.

Next, the prize awarding phase is entered, as will be described with reference to Fig. 5.

Firstly, a "win count" value is randomly selected, in this example being the 100th play. That is, it is not necessarily dependent upon the monetary value wagered, although the units wagered may be an additional factor. Every player of machines 1-5 thus has an opportunity to win the known prize value. It remains the requirement that the prize awarding phase is independent of the prize accumulation phase, and the player causing the prize accumulation phase to end will not, as a matter of course, be the winner.

Next, each input play event is counted by the central processor 54 and, importantly, the units wagered are separately accumulated in an "excess pool" within the memory 55.

The processor 54 then determines whether the number of input events is equal to (or has now passed) the win count. If not, the process repeats. If so, a win occurs and the identity of the gaming machine whose play achieves the equality is recorded

and displayed at least on the winning machine's display 24. An indication of the win also may be passed to a display or indicator associated with the winning gaming machine The player now is entitled to redeem the prize.

The cycle now passes to the general "continue" test shown in Fig. 3, and the excess pool value is added to the starting value as previously discussed.

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By arranging the bonus prize winning mode into two discrete phases, there is enhanced enjoyment and excitement for players in that once the bonus value is reached, and this fact is known, each player then has a chance to win that prize by continued play.

A further embodiment offers a modification to the sequence already described, in that the "win count" in the prize awarding phase can otherwise be selectable by a system operator as a set value. It will be remembered that the quantum of the prize has been previously determined by the system operator. This quantum can be a fixed amount, the amount in the bonus pool or a non-cash value. A yet further embodiment can be operated in the prize awarding phase so that the "plays" counted are only instances where the play results in a win (i.e. coin-out). Yet further, the inputs counted can be on the basis of the money wagered on each play (i.e. coin-in) or time.

The prize accumulation phase describes in relation to the first embodiment describes accumulation of percentage of coin-in. The accumulation equally could be on the basis of another parameter such as coin-out, games played or time (whether time played on a machine or absolute time). Many other parameters can be utilised as would be apparent to one skilled in the art.

A yet further embodiment adds a level of sophistication in the switching between the prize accumulation and prize awarding phases. It is open to determine which ones of the connected gaming machines can participate in the prize awarding phase. One strategy is to await a period of time in the range t=0-30 seconds to sample which connected machines are still active and permitting only those machines to participate in the prize awarding phase. Another strategy can be to allow only those

players that participated in the prize accumulation phase to participate in the prize awarding phase. Alternatively, it may be desired to make the prize awarding phase open to "all comers".

#### The claims defining the invention are as follows:

1. A method for awarding a bonus prize in the play of a plurality of gaming machines, the gaming machines being linked to a central processor having a data memory, the method comprising the steps of:

transmitting, on play of any one gaming machine, a play signal therefrom to said central processor, said play signal identifying the transmitting gaming machine;

registering each said play signal as an entry in said memory;

executing a prize accumulation phase during which a value is accumulated with an initial value for each said play signal up to a level where the accumulating value equals a prize value that will be awarded to one of the linked gaming machines; and

thereafter executing a prize awarding phase during which said accumulated value remains frozen, and subsequent plays of one or more of the gaming machines are separately counted for their occurrence, and a play of a gaming machine causing the count to equal a prize win count value results in that gaming machine be awarded said prize value.

- 2. A method as claimed in claim 1, whereby said play signals generated in the prize accumulation phase are based on any one or more of coin-in, coin-out, play duration and quantum of wagers for the respective gaming machine.
  - 3. A method as claimed in either one of claim 1 or claim 2, whereby said play signals generated in the prize awarding phase are based on any one or more of coinin, coin-out, play duration, number of game plays and quantum of wager for the respective gaming machines.
  - 4. A method as claimed in claim 3, whereby, in the prize awarding phase, the gaming machines contributing to the separate count includes any one of:



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- (i) all the gaming machines,
- (ii) only the gaming machines playing during the prize accumulation phase, or
- (iii) only the gaming machines playing during a short time period reckoned from commencement of the prize awarding phase.
  - 5. A method as claimed in any one of the preceding claims, including the further step of displaying said accumulated value on an indicator means linked to said central processor.
  - 6. A method as claimed in claim 5, comprising the further step of displaying the awarding of the prize value on the indicator means and/or the respective gaming machine.
  - 7. A method as claimed in any one of the preceding claims, further comprising the step of displaying the prize value during at least a portion of the prize awarding phase.
- 8. A method as claimed in any one of the preceding claims, further comprising the steps of:

continuing to add value for each play signal during the prize awarding phase; accumulating the added value during the prize awarding phase into an excess pool;

initiating a second prize accumulation phase after the prize awarding phase; and using the excess pool as at least a portion of an initial value during the second prize accumulation phase.



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9. A gaming machine system having a bonus prize awarding mode in the play of gaming machines, the system comprising:

a plurality of gaming machines for playing games thereon;

a plurality of interface units each coupled with a respective gaming machine to receive a play signal therefrom including the identity of the respective gaming machine;

a central processor having a data memory and being linked to said interface units to receive said play signals;

and wherein said system is operable by said central processor executing a first prize accumulation phase during which a value is accumulated with an initial value for each said play signal up to a level where the accumulating value equals a prize value that will be awarded to one of the linked gaming machines, and thereafter executing a second prize awarding phase during which said accumulated value remains frozen, and subsequent plays of the gaming machines are separately counted for their occurrence, and a play of a gaming machine causing the count to equal a prize win count value results in that gaming machine be awarded said prize value.

- 10. A system as claimed in claim 9, wherein said play signals generated in the prize accumulation phase are based on any one or more of coin-in, coin-out, play duration and quantum of wagers for the respective gaming machine.
- 11. A system as claimed in either one of claim 9 or claim 10, wherein said play signals generated in the prize awarding phase are based on any one or more of coinin, coin-out, play duration, number of game plays and quantum of wager for the respective gaming machines.
- 12. A system as claimed in claim 11, wherein, in the prize awarding phase, the gaming machines contributing to the separate count includes any one of:
  - (i) all the gaming machines,

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- (ii) only the gaming machines playing during the prize accumulation phase, or
- (iii) only the gaming machines playing during a short time period reckoned from commencement of the prize awarding phase.
- 13. A system as claimed in either one of claims 9 or 10, further comprising display means linked to said central processor on which said accumulating value is displayed.
- 14. A system as claimed in claim 13, wherein the awarding of a prize value is displayed on the indicator means and/or the respective gaming machine.
- 15. A system as claimed in claim 13, wherein the prize amount is displayed on said display means during at least a portion of the prize awarding phase.
- 16. A system as claimed in any one of claims 9 to 15, further operable to continue to add a value for each play signal during the prize awarding phase, accumulate the added value during the prize awarding phase into an excess pool, initiate a second prize accumulation phase after the prize awarding phase, and use the excess pool as at least a portion of an initial value during the second prize accumulation phase.
- 17. A method of operating gaming devices interconnected by a network to a central processor, comprising the steps of:

determining a prize amount;

transmitting an identifying signal from each gaming device to the central processor responsive to play of the gaming devices;

using the processor to track the amount of money played on the gaming devices;

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allocating aportion of the money played on the gaming devices to a bonus pool during a prize accumulation phase;

thereafter continuing to transmit an identifying signal from each gaming device to the central processor responsive to play of the gaming devices during a prize awarding phase;

comparing gaming device play during the prize awarding phase to a predefined level of gaming device play; and

awarding the prize amount to a selected gaming device after the gaming device play during the prize awarding phase equals the predefined level of gaming device play.

18. A method as claimed in claim 17, wherein said method further comprises the step of displaying the prize amount during at least a portion of the prize awarding phase.

19. A method as claimed in either one of claims 17 or 18, wherein said method further comprises the steps of:

continuing to use the processor to track the amount of money played on the gaming devices during the prized awarding phase;

allocating a portion of the money played on the gaming devices to an excess pool during the prize awarding phase; and

adding the excess pool to the bonus pool after the prize awarding phase.

20. A method of awarding a bonus prize substantially as herein described and as shown in the accompanying drawings.



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21. A gaming machine system substantially as herin described and as shown in the accompanying drawings.

DATED this twelfth Day of March, 2002

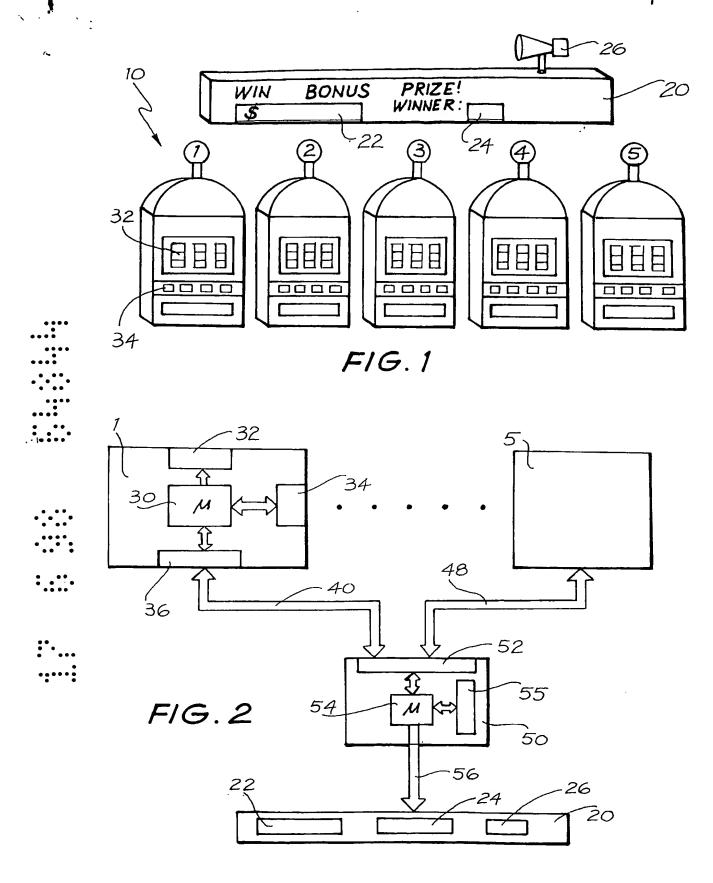
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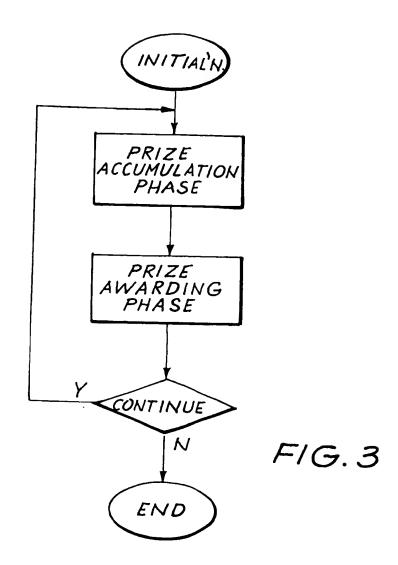
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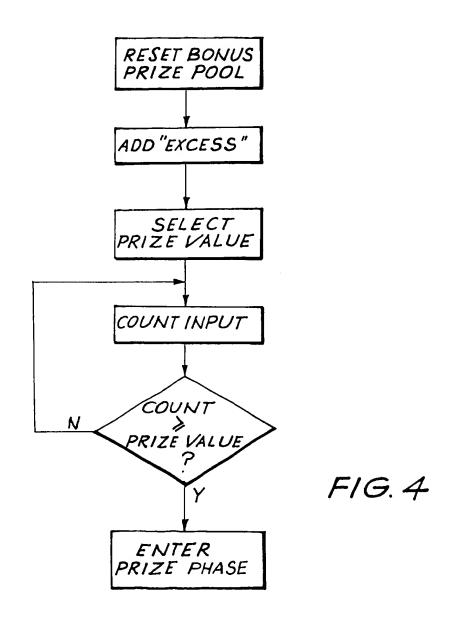








### PRIZE ACCUMULATION PHASE



## PRIZE AWARDING PHASE

